

Title : Re-reg Exam Time table

Bansilal Ramnath Agarwal Charitable Trust's
VISHWAKARMA INSTITUTE OF TECHNOLOGY, PUNE – 411037.
(An Autonomous Institute affiliated to University of Pune)

Academic Year: **2019-2020**Department: **E&TC**Date: **13/09/20**

Sr No	Candidate Name	Course Code and Name	Course In-charge/Examiner	Contact No.	Email-Id	Examination Date
1	BHAGAT MANJUSHRI SHIVAJI	EC20101 : SEMICONDUCTOR DEVICES AND CIRCUITS	PROF. M V WYAVAHARE	8806544466	Medha.wyavahare@vit.edu	05/10/20
2	BHAGAT MANJUSHRI SHIVAJI	EC20104 : SIGNAL AND SYSTEMS	PROF. K S DEGAONKAR	7517398911	Kishori.degaonkar@vit.edu	09/10/20
3	BHAGAT MANJUSHRI SHIVAJI	EC31101 : MECHATRONICS	PROF.P A KULKARNI	9822093643	Pooja.kulkarni@vit.edu	07/10/20
4	SHOAIB ABDULLAH ANSARI	EC20103 : LINEAR ALGEBRA AND STATISTICS	PROF. A A DIXIT	9665219635	Abha.dixit@vit.edu	05/10/20
5	SHOAIB ABDULLAH ANSARI	EC30102 : DIGITAL COMMUNICATION	PROF. S N RAUT	8999604806	Shital.raut@vit.edu	06/10/20
6	SHOAIB ABDULLAH ANSARI	EC33308 : LATEX	PROF. M V KAMBLE	9623551946	Milind.kamble@vit.edu	08/10/20
7	SHOAIB ABDULLAH ANSARI	EC33312 : SWITCH BASED SIMULATION	PROF. M V KAMBLE	9623551946	Milind.kamble@vit.edu	09/10/20
8	SHOAIB ABDULLAH ANSARI	EC30105 : ANALOG CIRCUITS	PROF. R V TORNEKAR	9096430212	Rupali.tornekar@vit.edu	07/10/20
9	SHOAIB ABDULLAH ANSARI	CH10101 : CHEMISTRY	Prof. Dr. Shrikant Kulkarni	9970663353	shrikaant.kulkarni@vit.edu	05/10/20
10	SHOAIB ABDULLAH ANSARI	CS10102 : COMPUTER PROGRAMMING	Prof. Kalpesh Joshi	9404972791	kalpesh.joshi@vit.edu	06/10/20
11	SHOAIB ABDULLAH ANSARI	HS10106 : ELECTRICAL TECHNOLOGY	Prof. Anita Joshi	9850833153	anita.joshi@vit.edu	07/10/20
12	SHOAIB ABDULLAH ANSARI	HS25324 : AEROBICS	Prof. Dr. Sachin Sawant	9527365062	sachin.sawant@vit.edu	08/10/20
13	SHOAIB ABDULLAH ANSARI	HS17401 : MINI PROJECT	Prof. Harshavardhan Khare	9423016776	harshavardhan.khare@vit.edu	09/10/20
14	DHARMADHIKARI JAYESH GANESH	ET 3003 : POWER ELECTRONICS	PROF. B G TARALEKAR	9403185042	Bharat.taralekar@vit.edu	05/10/20
10	DHARMADHIKARI JAYESH GANESH	ET3005 : VLSI DESIGN	PROF. J R MADKE	8308915400	Jyoti.madke@vit.edu	06/10/20

Instruction: All students are hereby informed to contact concerned course In-charge on or before 21/09/2020, without fail. For any query, contact undersigned.

Signature of Head of the Department / Department Exam coordinator
Name: Taralekar B. G. (ETC)