

Bansilal Ramnath Agarwal Charitable Trust's
**Vishwakarma Institute of Technology,
Pune**

(An Autonomous Institute Affiliated to Savitribai
Phule Pune University)

**Department of Electronics &
Telecommunication Engineering**

Workshop

On

**Machine Learning in
Speech Processing**

12th July to 15th July 2017

REGISTRATION FORM

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Workshop

On

**MACHINE LEARNING IN
SPEECH PROCESSING**

12th July to 15th July 2017

CONVENER

Prof. Dr. Shripad Bhatlawande

ORGANIZED BY,

Department of

**Electronics & Telecommunication
Engineering**



**Vishwakarma Institute of
Technology, Pune**

(An Autonomous Institute affiliated
to Savitribai Phule Pune University)

666, Upper Indira Nagar, Bibwewadi,

Pune -37.

Website : www.vit.edu

About the Institute and Department:

Vishwakarma Institute of Technology, Pune, a highly commendable private institute occupies a place of pride amongst the premier technical institutes of the western region of India. Established in the year 1983 and financed and run by Bansilal Ramnath Agrawal Charitable Trust, it is affiliated to Savitribai Phule Pune University. The Institute runs 9 under graduate, 8 post-graduate and 6 Ph.D. programs which are affiliated to the University. The Institution has been awarded ISO 9001-2015 certification by BSI, India and granted autonomous status since academic year 2008-09. The institute has marched towards the pinnacle of glory through its remarkable achievements and laurels in the field of engineering education. The department of Electronics and Telecommunication Engineering runs an UG, PG and Ph.D. programs and caters to the ever increasing needs of technical brilliance in all allied areas of Electronics and Telecommunication engineering.

About the workshop:

Speech processing has a long history and as a technology area it is intensively explored both by industry and academia. Speech based applications are quickly becoming more popular in today's world. To develop applications such as speech recognition, speaker recognition, language recognition, speech synthesis for interactive systems, speech based security systems,

speech enhancement, speech coding and compression, voice analysis for medical diagnosis etc. requires a broad base of fundamental knowledge and mastering in efficient algorithms and techniques. The main objective of this workshop is to provide a platform for Students, Teachers, Researchers and Industry personnel to learn the basics of speech processing and start research and development of applications in this field. This workshop is aimed at who want to do research in the Speech processing area, with motivation for building practical systems and applications using available tools and resources.

Contents:

- Speech production, perception
- Statistical Modeling of speech signal using GMM-HMM
- GMM based Speaker Recognition System
- HMM (HTK Toolkit) based Speech Recognition System
- Hands-on sessions on different software platforms

Important Dates:

Last Date for Registration: **30th June 2017**

Course Dates: **12th July to 15th July 2017**

Take Away:

Isolated Digit Recognition System using HMM (based on HTK Toolkit) on your own

Laptop

Objectives:

- To provide a pathway to access the speech processing techniques available to researchers.
- To transform small ideas into physically realizable systems.
- To build the community to work in the speech processing domain.

Target Participants:

Faculty members, Industry persons, Research scholars, UG & PG students in E&TC, Electronics, Instrumentation, Computer, IT Engineering, Instrumentation engineering.

Registration Fees:

For Faculty/ Research Scholars/ M. Tech Students: Rs. 2500/- (Excluding all Taxes).

Payment Details:

For payment details visit:

<http://www.vitvl.in/landingPage/workshop-on-machine-learning-in-speech-processing.php?CI=TlRrPQ>

(Registration is strictly on First come First allotted basis).

For Registration Contact:

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